CURRICULUM VITAE

Partha Sarathi Padhy
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MTECH in Electronic Information Systems with Six Years of Qualitative Experience Teaching ~Research ~ Student Counsellor

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CAREER PROFILE:

Looking to pursue a challenging and rewarding career in teaching profession with a dynamic and reputed organization, that would provide ample opportunities to learn, apply and enhance my knowledge and experience through practical exposure, for the overall growth of the organization as well as to the society.

® ORGANZATIONAL EXPERIENCE:

- ❖ Currently working as a Asst Professor in the Department of Electronics and communication Engineering at "Roland Institute of Technology" Berhampur, Odisha. from 10th Jan 2017 to till date.

- ❖ Worked as Resource Person in the Department of Electronics and Telecommunication Engineering at "Uma charana pattnaik engineering school (UCPES) Berhampur, Under Govt. of Odisha, Industries Dept. Affiliated to A.I.C.T.E Under DTE &T. Odisha. From 14th July 2014 to 09th Jan 2017.

- ❖ Worked as Asst Professor in the Department of Electronics and Telecommunication Engineering at "Rahul Institute of Engineering & Technology" (RIET) Berhampur, Odisha. from 11th Jan 2013 to 30th June 2014.

- ❖ Worked as Lecturer in the Department of Electronics and Telecommunication Engineering at "Hi-tech Institute of Information Technology" Umeri, jeypore (Koraput) an ISO 9001 certified Institute from 10th Dec 2010 to 10th Jan 2013.

- ❖ Worked as a Project Associate on project titled Software Development for RCS Prediction of Naval Ship using java at axis aerospace & technologies pvt.ltd. Bangalore from 1st june 2008 to 31th june 2009.

📖 Technical Skills:

Software:

- ▪ MATLAB, Scilab, XILINX, HFSS, CST, ADS
- ▪ Circuit Designe & Simulation using Orcad Pspice, VLSI Design.
- ▪ Languages : C, C++, core java.
- ▪ Operating Systems: Linux (Ubuntu) and windows (98, 2000, NT, XP and 7).
- ▪ Application Packages: MS Office, Photoshop Adobe PageMaker.

Hardware:

Maintenance and Troubleshooting.
Installing and configuring all the I/O devices and I/O cards.
Installing and configuring all the operating system like win 98, Win NT, win XP, win Vista, windows7.

📖 ACADEMIA:

Institution	Degree	Year	Subject
BPUT Odisha	Ph.D (Pursuing)		RF and Microwave
Berhampur University	MTech	2012	Electronic Information Systems
BPUT Odisha	BTech	2008	Electronics & Comm Engg
CHSE Odisha	12th	2003	English,MILOriya,Physics, Chemistry,Maths
BSE Odisha	10th	2001	FLO,SLE,TLH,Maths,Science

✍ CURRICULUM PROJECTS:

Project 1:   Design of Microstrip Patch Antenna
Duration: 6 months
Description: In this project an attempt has been made to fabricate a rectangular microstrip antenna using PCB board as substrate. Microstrip antennas are light in weight, easy to fabricate and operate at high frequency. The performance of the antenna is analyzed theoretically and experimentally.The result are in good agreement with each other.

Project 2:   Design of Microstrip Patch Antenna Using PSO Algorithm
Duration: 6 months
Description: In this project an attempt has been made to calculate the optimized dimension of microstrip patch antenna for desired frequency using particle swarm optimization technique. The PSO code is developed using 'C'. The PSO takes frequency of microstrip antenna as objective function. A local based PSO algorithm is used for this purpose. The dimensions obtained from PSO algorithm is compared with measured result. The results are in good agreement with each other.

📖 ACADEMIC EXCELLENCE

WORKSHOP/ FACULTY DEVELOPMENT PROGRAMME/ SHORT TERM COURSE ATTENDED:

1. A one week   AICTE sponsored Short Term Course on Recent Advances on passive and Active components at high Frequencies at IIT Varanasi (BHU) during 25th to 30th June 2018, Varanasi, Uttar Pradesh.
2. A Two-week A.I.C.T.E. sponsored Faculty Development Program on Efficient and Low Loss Antenna Configurations during 21th May to 02nd July 2018 at IIT Kharagpur under continuing education programme.

3. A Five-day QIP Short Term Course on Fundamentals of Digital Image and video Processing with 3D applications at IIT Kharagpur during15th to 19th may 2018.
4. A one week AICTE sponsored Short Term Course on HDL for Signal & Image Processing at IIT Varanasi (BHU) during 21th to 26th August 2017, Varanasi, Uttar Pradesh.
5. A Five-day QIP Short Term Course on Millimeter wave circuits and Antennas for short Range Communications at IIT Kharagpur during 19th to 23th June 2017.
6. A Two-week A.I.C.T.E. sponsored Faculty Development Program on Design of micro optical components using advanced software tool during 27th Nov to 09th Dec 2017 at GITA Bhubaneswar Odisha.
7. A one week QIP AICTE sponsored Short Term Course on Recent Trends in Antenna Analysis and Design at VSSUT Burla odisha during 15th to 20th May 2017.
8. Participated in the online course on Physics Of Semiconductors during 15th Aug to 20th Nov 2017 by IIT Kanpur, Uttar Pradesh.
9. Participated in the short term training programme on" Rural development and solid waste management through CDTT schme" during 05th to 09th Dec 2015 at NITTTER Kolkata.
10. Participated in the AICTE sponsored short term course on "Fundamentals and Applications of Metamatrials" during 06th to 10th April 2015 at I.I.T. Kanpur.
11. Participated in the AICTE sponsored short term course on "RF Circuits and systems for Industrial Applications" conducted by department of Electrical Engineering during September 08th to 12th 2014 at I.I.T. Kanpur
12. Participated in the ISTE-SRM short term training programme on "Challenges in Space Technology" organized by SRM University and sponsored by Indian Society for Technical Education, New Delhi and SRM University, Chennai, during 23rd to 27th June 2014.
13. Attended the AICTE sponsored short term course on "Systems Engineering" during March 24th to 28th 2014 at I.I.T. Kanpur
14. Participated in the MHRD-TEQIP-II sponsored faculty development programme on "Electromagnetics in VLSI" organized jointly by the department of Electronics and Communication Engineering and Centre for Continuing Education held at NIT Calicut, Kerala from 16th to 21th June 2014.

15. Participated in the two day faculty development programme on "Optical Engineering and Photonics" organized on 21th & 22nd march 2014 by Photonics Society India Chapter at Vignan Institute of Technology & management Berhampur.

16. Two-Week ISTE Main Workshop on "Signals and Systems" Under the National Mission on Education through ICT (MHRD, Govt. of India) Conducted by IIT Kharagpur from 2nd January to 12th January 2014 at GIFT Bhubaneswar.

17. Two-Week ISTE Main Workshop on "Control Systems" Under the National Mission on Education through ICT (MHRD, Govt. of India) Conducted by IIT Kharagpur from 2nd december to 12th December 2014 at GIFT Bhubaneswar.

18. Two-Week ISTE Main Workshop on "Analog Electronics "Under the National Mission on Education through ICT (MHRD, Govt. of India) Conducted by IIT Kharagpur from 4th June to 14th June 2013 at GEC Bhubaneswar.

19. Two day National Workshop on "Advanced tools and techniques for technical research" from 31st January to 1st February 2014 at Gandhi group of Institutions Berhampur.

20. 7th National Workshop on Emerging Technologies in Electrical Power Engineering (NWET-2014) from 27th February to 1st march 2014 at Silicon Institute of Technology, Bhubaneswar.

21. VLSI Signal Processing: Efficient Design & Implementation" from 15th to 18th march 2013 at Silicon Institute of Technology, Bhubaneswar.

22. Two Days workshop on VLSI & Embedded System, at RIT Berhampur during 15th to 16th April 2010.

23. Two Days workshop on Industrial Automation, at RIT Berhampur during 28th Feb to 1st March 2009.

24. Done training course entitled "Electronic Design Automation Software Tolls for VLSI/ASIC Design" at NIST Berhampur from 9th may 2007 to 5th June 2007.

25. Trained in Technical training programme at JITM Parlakhemundi from February 4th to 7th 2007.

MEMBERSHIPS IN PROFESSIONAL BODIES:

1. Member of IEEE (Membership No:94540071)
2. Member of International Association of Engineers - MIAENG (150429)
3. Life Member of ISTE - MISTE (LM 116210)
4. Life Member of International Society for Research & Development (ISRD M4150906116)
5. Associate Member (UACEE) of Institute of Research Engineers and Doctors (IRED) (AM101000582054)

AREA OF INTEREST:

- RF and Microwave Engineering
- Antenna and wave propagation
- Optical fiber communication systems
- Communication systems
- Signals and systems
- Semiconductor devices.
- High Frequency Engineering

ACADEMIC CONTRIBUTION:

Contributed for the Content Generation of the subject "Digital Signal Processing" under EDUSAT PROGRAMME of SCTE & VT, Odisha

OTHER ADMINISTRATIVE ACTIVITIES:
1. Examination incharge from 11th Jan 2013 to 30th June 2014 at RIET Berhampur.
2. Warden Hostels from 10th Dec 2010 to 10th Jan 2013 at HIIT Jeypore

INVOLVEMENT IN CERTIFICATION:
- Worked for ISO certification for the College.

- Worked for NBA certification for department.

- Involved in AICTE Affiliation as well as BPUT Affiliation work for the college.

REFERENCES:

Dr Dhruba Charan Panda
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Sri Debi Prasad Patnaik
HOD in the department of ETC
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(Govt. of Odisha)
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✐ PERSONAL DETAILS:

Father's Name   : Sri Narasingha Padhy.
Address   : At/Po: - Jayantipur (Temple Street)
                        Dist: - Ganjam (Odisha) Pin: - 761006
Date of Birth   : 15th July 1986.
Gender   : Male.
Nationality   : Indian
Marital Status   : Unmarried
Language Proficiency: English, Hindi, Oriya, Telugu and Bengali.
Hobbies   : Listening music, Gardening, Cooking, Making friends & Reading newspapers

✐ DECLARATION:

I hereby declare that the above information & particulars are true and correct to the best of my knowledge and belief.

Date: 15.07.18
Place: Berhampur   Partha Sarathi Padhy